

REV	Revision Notes	Designer	Approver	Date
A0-21	Initial release	VVDN	VVDN	10-02-2017

NXPU_MXEK

AUTOMOTIVE ETHERNET PHY RMII DAUGHTER CARD


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01	COVER PAGE
02	BLOCK DIAGRAM
03	TJA1100 BroadR-Reach PHY
04	ON BOARD CONNECTORS
05	CARD ID
06	MISCELLANEOUS

The symbols shown in green colour are no mount components

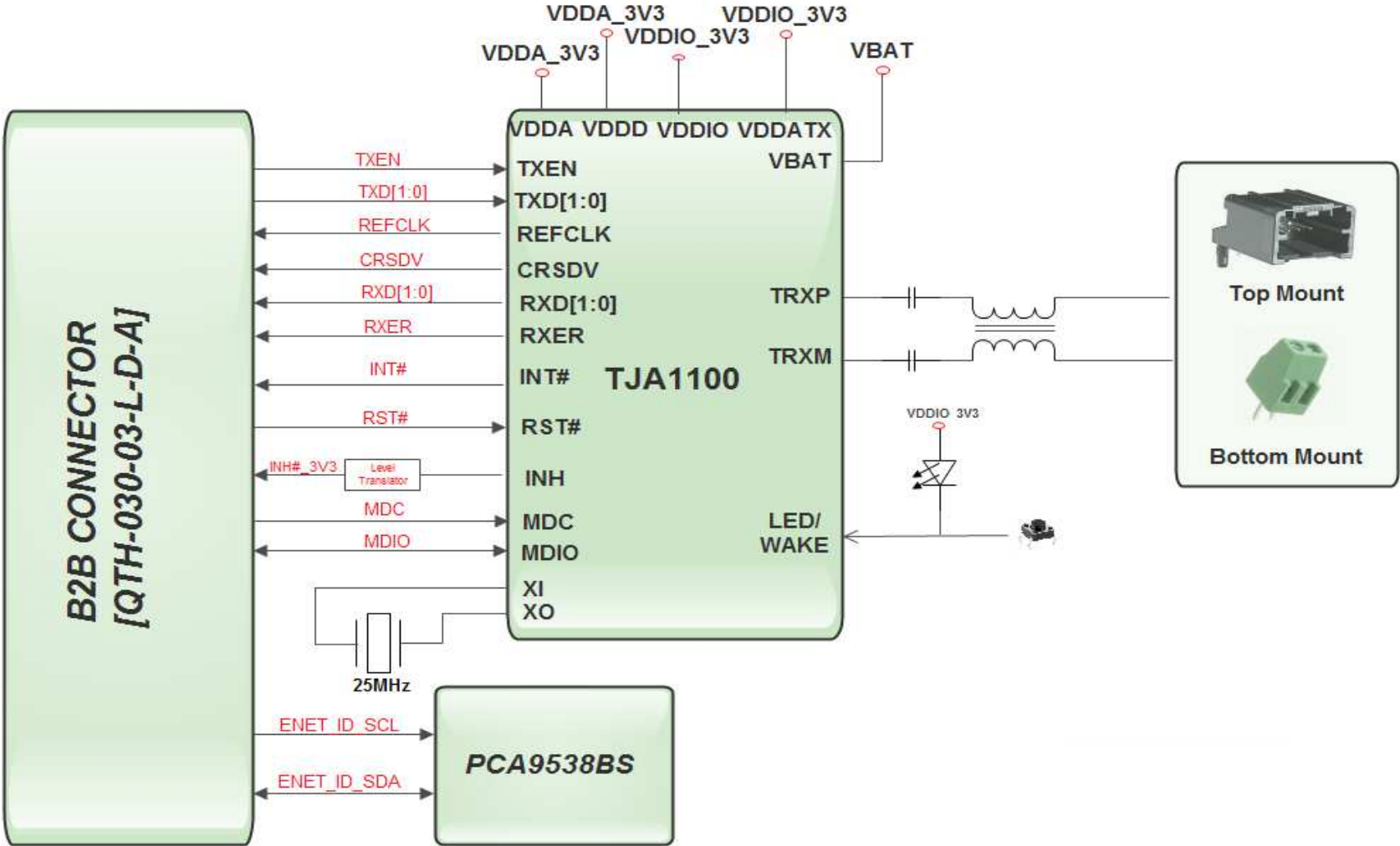
Kit part number: X-IMX-RMII-BRPHY

Note: "X-" is the prefix designator for prototype board part numbers.
For hardware at the production stage, there is no "X-" prefix.

NXPU MXEK

	A3	Title : COVER PAGE		
	Fab No : 501-1-00550			Rev: A0-21
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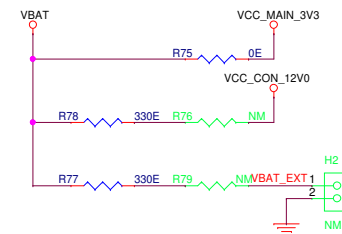
BLOCK_DIAGRAM



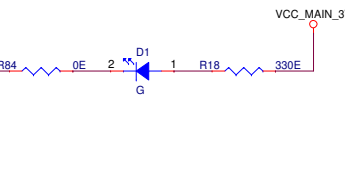
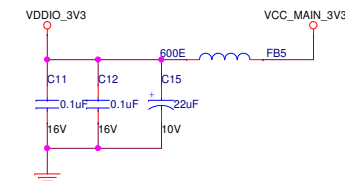
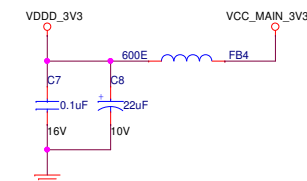
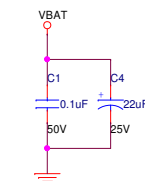
A3	Title : BLOCK DIAGRAM	
Fab No : 501-1-00550		Rev: A0-21
Asy No : 701-1-00640		Sheet 2 of 6

TJA1100 Automotive Ethernet PHY

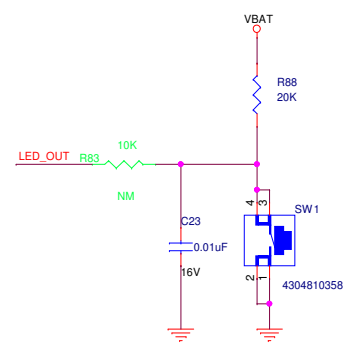
VBAT SELECTION



12-V option resistors are NXP use only for PHY INH power control experiments. 330-ohm resistors provide current-limit protection for daughter card.



LOCAL WAKE

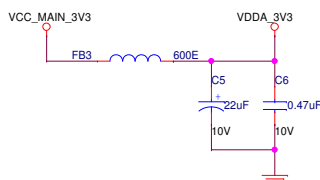
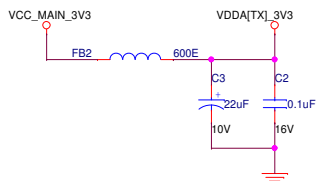


LED OUT Function is default. For WAKE input remove R84 and mount R83.

NXP/MXEK

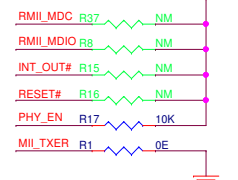
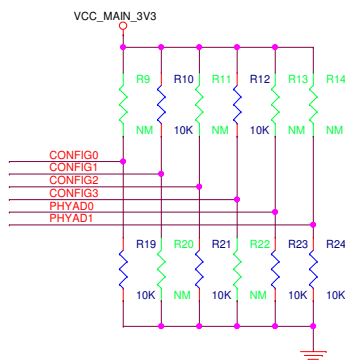


A3	Title : TJA1100 BroadR-Reach PHY	
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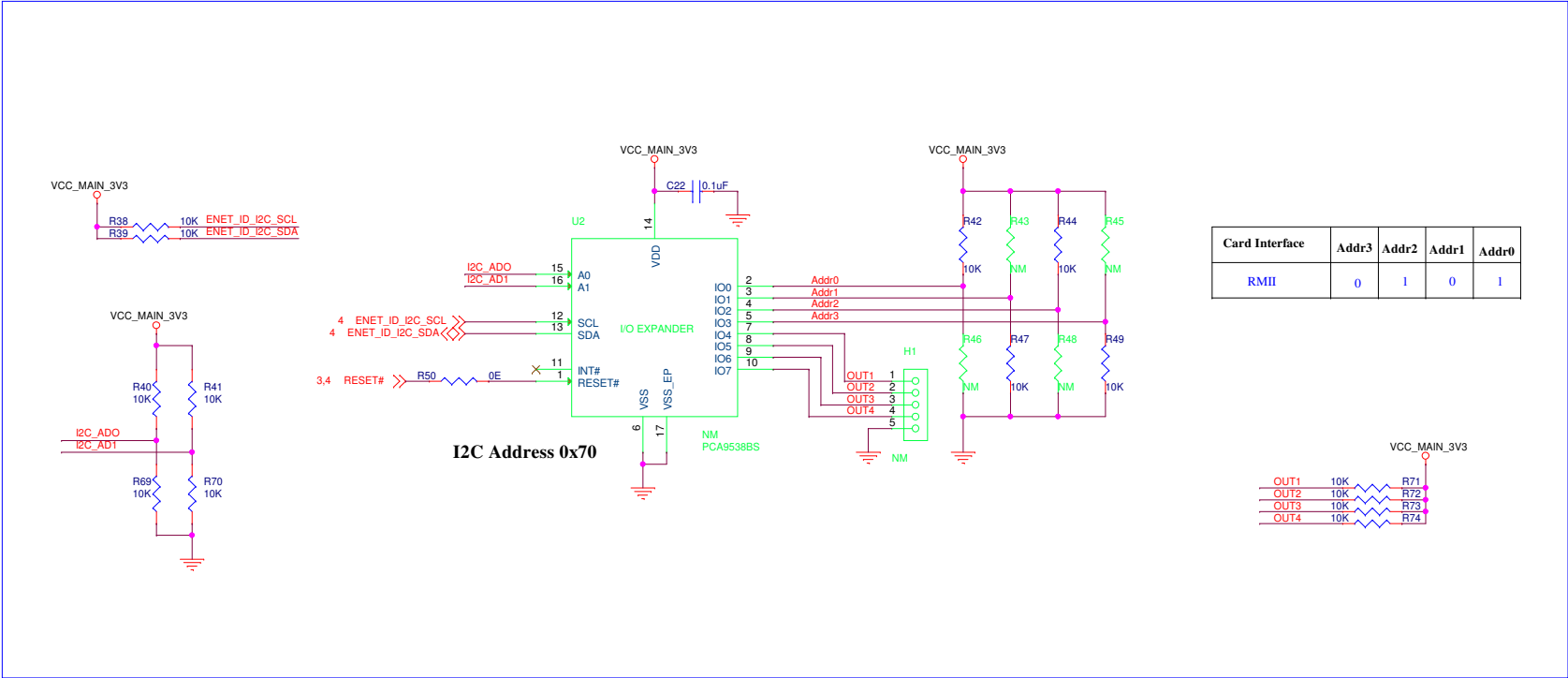
Hardware Strapping Options

Pin Name	Config	Selected Mode
CONFIG0	LOW	PHY configured as Slave
CONFIG1	HIGH	Autonomous operation
CONFIG2	LOW	RMII mode (25 MHz crystal)
CONFIG3	HIGH	
PHYAD0	LOW	PHY address bits 0 & 1
PHYAD1	LOW	



NOTE: For open-drain outputs, pull-up resistors are typically on CPU Cards by default.

CARD ID DETECTION



MISCELLANEOUS

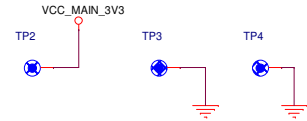
NOTE: THIS PAGE CONTAINS

- 1) GLOBAL FIDUCIALS
- 2) TEST POINTS
- 3) MOUNTING HOLES
- 4) BPCB

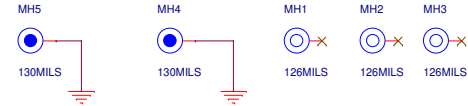
GLOBAL FIDUCIALS



TEST POINTS



MOUNTING HOLES



BPCB



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